IN THE UNITED STATES PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. 13692

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AUG 3 1 2001

Technology Center 2600

Serial No.

09/870,841

Group Art Unit:

2661

Filed:

June 1, 2001

Examiner:

For:

CELL-BASED SWITCH FABRIC WITH CELL-TO-LINE-CARD CONTROL FOR REGULATING INJECTION OF PACKETS

atent Application of Richard S. NORMAN et al.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

This Information Disclosure Statement is being filed in the manner prescribed by 37 CFR 1.97(b) - (d) to satisfy the duty under 37 CFR 1.56 to disclose to the Office information, known to individuals associated with the filing and prosecution of the

subject application, which is material to the examination of the application.

In accordance with 37 CFR 1.97(g) and (h), this statement is not to be construed as a representation that a search has been made or an admission that the information cited

herein is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b).

This information disclosure statement is being filed within three months of the filing date

of a national application, within three months of the date of entry of the national stage

as set forth in 37 CFR 1.491 in an international application; or before the mailing date of

a first official action on the merits and therefore applicant respectfully requests

consideration under 37 CFR 1.97(b).

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In compliance with 37 CFR 1.98(a)(1), a list of all patents, publications or other

information submitted for consideration by the Office is hereby provided by way of the

attached Form PTO 1449.

In compliance with 37 CFR 1.98(a)(2), also enclosed is a legible copy of:

i) each United States and foreign patent;

ii) each publication or that portion which caused it to be listed; and

iii) all other information or that portion which caused it to be listed, excluding

any copies of a United States patent application.

It is respectfully requested that the information be expressly considered by the

Examiner and that the references be made of record and appear among the

"References Cited" on any patent to issue therefrom.

The Patent Office is hereby authorized to charge any deficiency, or credit any

overpayment in fees to Deposit Account Number

Respectfully submitted,

Ralph A. Dowell, Reg. No. 26,868

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Encls: Form PTO-1449

All references listed on Form PTO-1449

Acknowledgement Card

Docket Number (Optional) Application Number 09/870,841 13692 INFORMATION DISCLOSURE CITATION Applicant(s) Richard NORMAN et al. Use several sheets if **excessary**) Group Art Unit Filing Date AUG 2 8 2001 June 1, 2001 2661 U.S. PATENT DOCUMENTS FILING DATE EXAMINER SUBCLASS CLASS DOCUMENT NUMBER DATE NAME INITIAL IF APPROPRIATE 18/07/1989 1. U.S. 4,849,751 Barber et al. U.S. 5,072,366 10/12/1991 Simcoe 2. U.S. 4,955,020 04/09/1990 Stone et al. 3. FOREIGN PATENT DOCUMENTS Translation CLASS SUBCLASS COUNTRY DATE REF DOCUMENT NUMBER YES NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) French, R., Architectural Consideration for Internet Routers; retrieved from the internet guideline in file; Internet URL www.cise.ufl.edu/ rfrench, accessed July 23, 2001; 4. Joseph Desposito; Router-On-A-Chip Manages Network Traffic with Wire-Speed QoS; Electronic Design; May 1, 2000; pp 64-65-66; 5. DATE CONSIDERED **EXAMINER** EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. P09A/REV04 Patent and Trademark Office * U.S. DEPARTMENT OF COMMERCE Form PTO-A820

			Docket Number (Optional) 13692	Application Number
in out of the control			Applicant(s) Richard NORMAN et al.	09/870,841
	((Use several sheets if necessary)	Filing Date	Group Art Unit
			June 1, 2001	2661
*EXAMINER OTHER DOCUMENTS (Including Author, Tite			le, Date, Pertinent Pages, Etc.)	
OPE	15	Werner Bux et al.; Technologies and Building Blocks for Fast Packet Forwarding; IEEE Communications Magazine; January 2001; pp. 70-77.		
AUG 2 8 200	A OFFICE	Minagawa, N. et al.; Dept. of Comput. Scil, University of Electro-Commun. Tokyo, Japan; Implementation of a network switch on chips;(Abstract) Communications, vol. 13, no. 1; retreived on March 16, 2001 from INSPEC database.		
A TRADE	8.	Saturn: a terabit packet switch using dual round-robin; (abstract) Globecom'00 - IEEE, Global Telecommunications Conference; Dept. of Electr. Eng. Polytech, Univ.of Brooklyn, NY, U.S.A.; retrieved on June 4, 2001 from INSPEC database.		
	9.	Nanette J. Boden et al.; Myrinet - Gigabit-per-Second Local-Area Network [on line]; November 16, 1994 Myricom, Inc.; Internet URL http;//www.myrinet.com/research/publications/Hot.ps; retrieved on March 14, 2001;		
	10.	Vitesse Semiconductor Corporation [on line]; Datasheet VSC880; January 5, 2001; pp. 1-20; retrieved on July 23, 2001; Internet URL www.vitesse.com/products/documents.cfm.family= document-id=180;		
	11.	Vitesse Semiconductor Corporation [on line]; Dat Internet URL www.vitesse.com	asheet VSC870; June 29, 2001; pp. 1	-40; retrieved July 23, 2001;
	12.	A New Architecture for Switch and Router Design; PMC-Sierra Inc.; December 22, 1999; Internet URL http://www.pmcsierra.com/pressRoom/pht/1cs_wp.pdf retrieved on July 4, 2001; pp. 1-8;		
	13.	Network Processor Designs for Next-Generation Networking Equipment [on line]; EZ Chip Technologies; Internet URL http://www.ezchip.com/images/pdfs/etchip_white_paper.pdf; retrieved on July 4, 2001; December 1999; pp.1-4. Cyrel Minkenberg et al. A combined Input an dOutput Queued Packet-Switched System Based on Prizma Switch-on-a-Chip Technology; Scalable High-Speed Switches/Routers with QoS Support; IBM Research, Zurich Research Laboratory; IEEE Communications Magazine; December 2000; pp 70-84; Werner Bux et al.; Technologies and Building Blocks for Fast Packet Forwarding; Telecommunications Networking at the Start of the 21st Century; IEEE Communications Magazine; January 2001; pp 70-77; Child, J.; Bus-switching chip busts bandwidth barrier [on line]; Internet URL http://www.computer-design.com/editorial/1995/06/directions/bus.html; retrieved on March 15, 2001.		
	14.			
	15.			
	16.			
T	17.	PSID - Based Communications Switching [on line]; December 1997; Internet URL http://www.icube.com/commsw.pdf; retrieved on March 15, 2001; pp 1-14		
EXAMINER			DATE CONSIDERED	
		citation considered, whether or not citation is in conforma copy of this form with next communication to applicant.	unce with MPEP Section 609; Draw line the	nrough citation if not in conformance and